



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **YONEDA, Yoshiyuki et al.**

Group Art Unit: **2827 (expected)**

Serial No.: **10/650,692**

Examiner: **David A. ZARNEKE (expected)**

Filed: **August 29, 2003**

For. **MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE  
INCORPORATING A PASSIVE ELEMENT AND A REDISTRIBUTION BOARD**

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: October 20, 2003

Sir:

This is a Preliminary Amendment for the above-captioned patent application. Please  
amend the above-captioned patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of  
this paper.

**Remarks/Arguments** begin on page 5 of this paper.